



*Summer Workshop on* **VHDL Programming**  
**(4<sup>th</sup> to 5<sup>th</sup> July 2018)**

***Conducted by ECE & EEE Department***

- **Workshop Focus:**
  - ✓ VHDL overview
  - ✓ VHDL Characteristics and Concepts
  - ✓ Basic VHDL modelling
  - ✓ Testbench
  - ✓ Concurrent Statements
  - ✓ Sequential Statements
  - ✓ Implementation of basic units

**Conducted by**  
**Ms. Payal Garg**

To register click [here](#)  
or scan the QR code

