DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, LUCKNOW

EVALUATION SCHEME & SYLLABUS
FOR

B. TECH. 2nd YEAR

ELECTRICAL & ELECTRONICS ENGINEERING

BASED ON

AICTE MODEL CURRICULUM

[Effective from the Session: 2019-20]

EVALUATION SCHEME - B.TECH 2ⁿᵈ YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)

SEMESTER- III

Sl. No.	Subject Codes	Subject	Periods			Evaluation Scheme				End Semester		Total	Credit
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KOE307	Analog Devices & Electronic Circuits	3	0	0	30	20	50		100		150	3
2	KAS301	Technical Communication	2	0	2	30	20	50		100		150	3
3	KEE301	Electromagnetic Field Theory	3	1	0	30	20	50		100		150	4
4	KEE302	Electrical Measurements & Instrumentation	3	1	0	30	20	50		100		150	4
5	KEE303	Basic Signals & Systems	3	1	0	30	20	50		100		150	4
6	KEE351	Analog Electronics Lab	0	0	2				25		25	50	1
7	KEE352	Electrical Measurements and Instrumentation Lab	0	0	2				25		25	50	1
8	KEE353	Electrical Workshop	0	0	2				25		25	50	1
9	KEE354	Mini Project or Internship Assessment*	0	0	2			50				50	1
10	KNC301/ KNC302	Cyber Security/Environmental Science	2	0	0	15	10	25		50			NC⁺
11		MOOCs (Essential for Hons. Degree)											
		Total	**14**	**3**	**10**							**950**	**22**

The Mini Project or internship (3-4 weeks) conducted during summer break after II semester and will be assessed during III semester.

SEMESTER IV

Sl. No.	Subject Codes	Subject	Periods			Evaluation Scheme				End Semester		Total	Credit
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KAS401	Maths-IV	3	1	0	30	20	50		100		150	4
2	KVE401	Universal Human Values	3	0	0	30	20	50		100		150	3
3	KEE401	Digital Electronics	3	0	0	30	20	50		100		150	3
4	KEE402	Electrical Machines-I	3	1	0	30	20	50		100		150	4
5	KEE403	Networks Analysis & Synthesis	3	1	0	30	20	50		100		150	4
6	KEE451	Circuit Simulation Lab	0	0	2				25		25	50	1
7	KEE452	Electrical Machines-I Lab	0	0	2				25		25	50	1
8	KEE453	Digital Electronics Lab	0	0	2				25		25	50	1
9	KNC402/ KNC401	Environmental Science/Cyber Security	2	0	0	15	10	25		50			
10		MOOCs (Essential for Hons. Degree)											
		Total	**17**	**3**	**6**							**900**	**21**

ANALOG DEVICES & ELECTRONIC CIRCUITS

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Choose proper semiconductor device for various applications.	K_3
CO2	Analyze the Special Diodes and Power Devices with their characteristics and applications.	K_4
CO3	Analyze the structure operation, V-I characteristics and detail circuit of BJT with help of small signal model.	K_4
CO4	Analyze the structure operation, V-I characteristics and detail circuit of MOSFET with help of small signal model.	K_4
CO5	Demonstrate the use of Op-Amp circuits and its internal parameters along with its applications.	K_3

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)

K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I
Semiconductors: Charge carriers, Hall effects.
Diode: P-N Junction, Current Equation, Equilibrium condition, Forward & Reverse biased junction, Junction-capacitance breakdown characteristic, I-V Characteristics of a diode, Review of half-wave and Full-wave rectifiers, Zener diodes, Clamping and Clipping circuit.

Unit II
Special Diodes: LED, Photo-diode, Schottky diode, Tunnel diode, their characteristics and applications.
Uni Polar Devices: FET, MOSFET.
Introduction to Power devices: Characteristics of SCR, TRIAC, DIAC. UJT.

Unit III
BJT: Review of device structure operation and V-I characteristics, BJT circuits at DC, BJT as amplifier and switch, biasing in BJT amplifier circuit, small-signal operation and models, single stage BJT amplifier, BJT internal capacitances and high frequency model, frequency response of CE amplifier.

Unit IV
MOSFET: Device structure and its operation in equilibrium, V-I characteristics. Circuits at DC, MOSFET as Amplifier and switch, Biasing in MOS amplifier circuits, small-signal operation and models, single stage MOS amplifier, MOSFET internal capacitances and high frequency model, frequency response of CS amplifier.

Unit V
Ideal Op-Amp, non-idealities in an Op-Amp (Output offset voltage, input bias current, input offset current, slew rate, gain bandwidth product)
Idealized analysis of Op-Amp circuits. Inverting and non-inverting amplifier, differential amplifier, instrumentation amplifier, Integrator, Differentiator.

Text Books:
1. Robert L. Boylestad, Louis Nashelsky, "Electronic Devices And Circuits Theory" Pearson India.

2. AS Sedra and K.C. Smith "Microelectronics Circuits" Oxford University Press (India)
3. R. A. Gayakwad "Op amps and Linear Integrated Circuits" Prentice Hall of India.
4. Balbir Kumar and Shail B. Jain, "Electronic Devices and Circuits" Prentice Hall of India, 2007
5. BG Streetman and S. Banerjee, "Solid State Electronics Devices" Prentice Hall of India.

Reference Books:
1. Robert L. Boylestad, Louis Nashelsky, "Electronic Devices And Circuits Theory" Pearson India
2. Millman, J. and Christos Halkias, "Electronics Devices and Circuits" TataMcGraw Hill.
3. Aloke. K. Dutta, "Semiconductor Devices and circuits", Oxford University Press, 2008.

ELECTROMAGNETIC FIELD THEORY

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Apply different coordinate systems and their application in electromagnetic field theory, establish a relation between any two systems and also understand the vector calculus.	K_3
CO2	Understand the concept of static electric field. Understand the concept of current and properties of conductors. Establish boundary conditions and to calculate capacitances of different types of capacitors	K_4
CO3	Understand the concept of static magnetic field, magnetic scalar and vector potential	K_4
CO4	Understand the forces due to magnetic field, magnetization, magnetic boundary conditions and inductors.	K_4
CO5	Understand displacement current, time varying fields, propagation and reflection of EM waves and transmission lines.	K_3

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I
Coordinate Systems and Transformation: Basics of Vectors: Addition, subtraction and multiplications; Cartesian, Cylindrical, Spherical transformation. Vector calculus: Differential length, area and volume, line surface and volume integrals, Del operator, Gradient, Divergence of a vector, Divergence theorem, Curl of a vector, Stokes's theorem, Laplacian of a scalar.

Unit II
Electrostatic fields: Coulombs law and field intensity, Electric field due to charge distribution, Electric flux density, Gausses' Law- Maxwell's equation, Electric dipole and flux line, Energy density in electrostatic fields, Electric field in material space: Properties of materials, convection and conduction currents, conductors, polarization in dielectrics, Dielectric-constants, Continuity equation and relaxation time, boundary conditions, Electrostatic boundary value problems: Poisson's and Laplace's equations., Methods of Images.

Unit III

Magneto statics : Magneto-static fields, Biot - Savart's Law, Ampere's circuit law, Maxwell's equation, Application of ampere's law, Magnetic flux density- Maxwell's equation, Maxwell's equation for static fields, magnetic scalar and vector potential.

Unit IV
Magnetic forces: Materials and devices, Forces due to magnetic field, Magnetic torque and moment, a magnetic dipole. Magnetization in materials, Magnetic boundary conditions, Inductors and inductances, Magnetic energy.

Unit V
Waves and Applications: Maxwell's equation, Faraday's Law, transformer and motional electromotive forces, Displacement current, Maxwell's equation in final form Electromagnetic wave propagation: Wave propagation in loss dielectrics, Plane waves in lossless dielectrics Plane wave in free space. Plain waves in good conductors, Power and the pointing vector, Reflection of a plain wave in a normal incidence. Transmission Lines and Smith Chart.

Text Book: 1. MNO Sadiku, "Elements of Electromagnetic', Oxford University Press.

Reference Books: 1. WH Hayt and JA Buck, "Engineering Electromagnetic", McGraw- Hill Education.

ELECTRICAL MEASUREMENTS & INSTRUMENTATION

Pre-requisites of course: Basic Electrical Engineering

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Evaluate errors in measurement as well as identify and use different types of instruments for the measurement of voltage, current, power and energy.	K_1
CO2	Display the knowledge of measurement of electrical quantities resistance, inductance and capacitance with the help of bridges.	K_2
CO3	Demonstrate the working of instrument transformers as well as calculate the errors in current and potential transformers.	K_2
CO4	Manifest the working of electronic instruments like voltmeter, multi-meter, frequency meter and CRO.	K_2
CO5	Display the knowledge of transducers, their classifications and their applications for the measurement of physical quantities like motion, force, pressure, temperature, flow and liquid level.	K_3

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I
Electrical Measurements: Measurement system, Characteristics of instruments, Methods of measurement, Errors in Measurement & Measurement standards, Review of indicating and integrating instruments: Voltmeter, Ammeter and Wattmeter.

UNIT II
Measurement ofResistance, Inductance and Capacitance: Measurement of low, medium and high resistances, insulation resistance measurement, AC bridges for inductance and capacitance measurement.

UNIT III
Instrument Transformers: Current and Potential transformer, ratio and phase angle errors, design considerations and testing.

UNIT IV
Electronic Measurements: Electronic instruments: Voltmeter, Multimeter, Wattmeter & energy meter. Time, Frequency and phase angle measurements using CRO; Storage oscilloscope, Spectrum & Wave analyzer,Digital counter, frequency meter,and Digital Voltmeter.

UNIT V
Instrumentation: Transducers& sensors,classification & selection of sensors, Measurement of force using strain gauges, Measurement of pressure using piezoelectric sensor, Measurement of temperature using Thermistors and Thermocouples, Measurement of displacement using LVDT, Measurement of position using Hall effect sensors.Concept of signal conditioning and data acquisition systems, Concept of smart sensors and virtual instrumentation.

Text Book:
1. A K Sawhney, "Electrical & Electronic Measurement & Instrument", DhanpatRai&Sons, India
2. BC Nakra& K. Chaudhary, "Instrumentation, Measurement and Analysis," Tata McGraw Hill 2nd Edition
3. Purkait, "Electrical & Electronics Measurement & Instrumentation", TMH

Reference Books:
1. Forest K. Harris, "Electrical Measurement", Willey Eastern Pvt. Ltd. India
2. M. Stout , "Basic Electrical Measurement", Prentice Hall of India
3. WD Cooper, "Electronic Instrument & Measurement Technique", Prentice Hall International
4. EW Golding & F.C. Widdis, "Electrical Measurement &Measuring Instrument", AW Wheeler & Co. Pvt. Ltd. India

BASIC SIGNAL & SYSTEMS

Pre-requisites of course: Basic Electrical Engineering, Engineering Mathematics

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Represent the various types of signals & systems and can perform mathematical operations on them.	K_2
CO2	Analyze the response of LTI system to Fourier series and Fourier transform and to evaluate their applications to network analysis.	K_4
CO3	Analyze the properties of continuous time signals and system using Laplace transform and determine the response of linear system to known inputs.	K_4
CO4	Implement the concepts of Z transform to solve complex engineering problems using difference equations.	K_3
CO5	Develop and analyze the concept of state-space models for SISO & MIMO system.	K_4

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I

Pre- Requisites: *Differential Equations.*

Introduction to Continuous Time Signals and Systems: Introduction to continuous time and discrete time signals, Classification of signals with their mathematical representation and characteristics. Transformation of independent variable, Introduction to various type of system, basic system properties.
Analogous System: Linear& Rotational mechanical elements, force-voltage and force-current analogy,
modeling of mechanical and electro-mechanical systems: Analysis of first and second order linear systems by classical method.

UNIT II
Pre- Requisites: *Fourier Series & Fourier Transform*
Fourier Transform Analysis: Exponential form and Compact trigonometric form of Fourier series, Fourier symmetry, Fourier transform: Properties, application to network analysis. Definition of DTFS, and DTFT, Sampling Theorem.

UNIT III
Pre- Requisites: *Laplace Transform*
Laplace Transform Analysis: Review of Laplace Transform, Properties of Laplace Transform, Initial & Final value Theorems, Inverse Laplace Transform, Convolution Theorem, Impulse response, Application of Laplace Transform to analysis of networks, waveform synthesis and Laplace Transform to complex waveforms

UNIT IV
Pre- Requisites:*Matrix Calculations.*

State – Variable analysis: Introduction, State Space representation of linear systems, Transfer function and state Variables, State Transition Matrix, Solution of state equations for homogeneous and non-homogeneous systems, Applications of State – Variable technique to the analysis of linear systems.

UNIT V
Pre- Requisite: *Z-Transforms.*

Z – Transform Analysis: Concept of Z – Transform& ROC, Z – Transform of common functions,
Inverse Z – Transform, Initial & Final value Theorems, Applications to solution of difference equations, Properties of Z-transform.

Text Books:
1. Oppenhiem, Wilsky, Nawab, "Signals & Systems", PHI
2. Anand Kumar, "Signals & Systems", PHI
3. Choudhary D. Roy, "Network & Systems", Wiley Eastern Ltd.

Reference Books:
1. David K. Cheng; "Analysis of Linear System", Narosa Publishing Co
2. Donald E. Scott, "Introduction to circuit Analysis" Mc. Graw Hill
3. BP Lathi, "Linear Systems & Signals" Oxford University Press, 2008.
4. IJ Nagrath, S.N. Saran, R. Ranjan and S. Kumar, "Signals and Systems", TataMc.Graw Hill, 2001.
5. ME Van-Valkenberg; " Network Analysis", Prentice Hall of India

ANALOG ELECTRONICS LAB

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Understand the characteristics and applications of the Semiconductor devices.	K_2, K_3
CO2	Draw the characteristics of BJT, FET and MOSFET.	K_2, K_4
CO3	Understand the parameters of Operational Amplifier and instrumentation Amplifier with their applications.	K_2, K_4
CO4	Understand the V-I characteristics of Power devices like SCR, TRIAC.	K_2, K_4

KL- Bloom's Knowledge Level ($K_1, K_2, K_3, K_4, K_5, K_6$)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

1. To Plot V-I characteristics of P-N junction diode and Zener diode.

2. To draw wave shape of the electrical signal at input and output points of the half wave, full wave and bridge rectifiers.

3. To Plot input / output characteristics for common base transistor.

4. To determine voltage gain, current gain, input impedance and output impedance and frequency response of R-C coupled common emitter amplifier.

5. To Plot input /output characteristics of FET and determine FET parameters at a given operating point.

6. To Plot input /output characteristics of MOSFET and determine MOSFET parameters at a given operating point.

7. To study transistor as a switch and determine load voltage and load current when the transistor is ON.

8. Measurement of Operational Amplifier Parameters: Common Mode Gain, Differential Mode Gain, CMRR, Slew Rate.

9. Applications of Op-amp: Op-amp as summing amplifier, Difference amplifier, Integrator and differentiator.

10. Study of Instrumentation Amplifier.

11. To plot V-I characteristics of SCR.

12. To plot V-I characteristics of TRIAC.

ELECTRICAL MEASUREMENT AND INSTRUMENTATION LAB

Pre-requisites of course: Basic Electrical Engineering

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Understand the importance of calibration of measuring instruments.	K2
CO2	Demonstrate the construction and working of different measuring instruments.	K3
CO3	Demonstrate the construction and working of different AC and DC bridges, along with their applications.	K3
CO4	Ability to measure electrical engineering parameters like voltage, current, power & phase difference in industry as well as in power generation, transmission and distribution sectors.	K2
CO5	Capability to analyze and solving the variety of problems in the field of electrical measurements.	K2

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Note : Minimum ten experiments are to be performed from the following list:

1. Calibration of AC voltmeter and AC ammeter.
2. Measurement of inductance using Maxwell's Bridge.
3. Measurement of capacitance using Schering Bridge.
4. Measurement of low resistance using Kelvin's Double Bridge.
5. Measurement of Power using CT and PT.
6. Measuring displacement using LVDT.
7. Measuring temperature using thermocouple.
8. Measuring pressure using piezoelectric pick up.
9. Measurement of speed of DC motor by photoelectric pick up.
10. Speed measurement using Hall Effect sensor.
11. PC based data logging of temperature sensor using LabVIEW/ MATLAB.
12. Signal conditioning of analog signal using LabVIEW/ MATLAB.

ELECTRICAL WORKSHOP

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Perform various types of Electrical connections.	K_3
CO2	Develop small circuits on PCB	K_6
CO3	Differentiate between various electrical wires, cables and accessories.	K_3
CO4	Demonstrate the layout of electrical substation & various safety measures.	K_2

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

Note: Minimum ten experiments are to be performed from the following list:
1. To study the working and Control of two lamps in series and in parallel
2. To perform the stair case working and it's testing.
3. To study the working principle and wiring of fluorescent lamp.
4. To study and wiring of distribution board including power plug using isolator, MCB, ELCB.
5. To study and estimate a typical, BHK house wiring.
6. Familiarization, soldering, testing and observing the wave forms on CRO of a HW and FW uncontrolled rectifier (using diodes) with capacitor filter.
7. Visit your college substation and familiarize the supply system, Transformer, HT Paneland Distribution etc.
8. To study construction, working and application of workshop tools. Also study the Electrical and Electronics Symbols.
9. To study the wires, cables and their gauges, Domestic Electrical Accessories.
10. Mini Project on PCB.
11. To study fault, Remedies in Domestic Installation and Indian Electricity Rules.
12. To study the different types of earthing system and measure the earth resistance.

DIGITAL ELECTRONICS

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Apply concepts of Digital Binary System and implementation of Gates.	K_3
CO2	Analyze and design of Combinational logic circuits.	K_4
CO3	Analyze and design of Sequential logic circuits with their applications.	K_4
CO4	Implement the Design procedure of Synchronous & Asynchronous Sequential Circuits.	K_3
CO5	Apply the concept of Digital Logic Families with circuit implementation.	K_3

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus

UNIT I
Digital System And Binary Numbers: Number System and its arithmetic, Signed binary numbers, Binary codes, Cyclic codes, Hamming Code, the map method up to five variable, Don't care conditions, POS simplification, NAND and NOR implementation, Quine McClusky method (Tabular method).

UNIT II
Combinational Logic: Combinational Circuits: Analysis Procedure, Design procedure, Binary adder-subtractor, Decimal adder, Binary multiplier, Magnitude comparator, Multiplexers, Demultiplexers, Decoders, Encoders.

UNIT III
Sequential Logic And Its Applications: Storage elements: latches & flip flops, Characteristic Equations of Flip Flops, Flip Flop Conversion, Shift Registers, Ripple Counters, Synchronous Counters, Other Counters: Johnson & Ring Counter.

UNIT IV
Synchronous & Asynchronous Sequential Circuits: Analysis of clocked sequential circuits with state machine designing, State reduction and assignments, Design procedure. Analysis procedure of Asynchronous sequential circuits, circuit with latches, Design procedure, Reduction of state and flow table, Race-free state assignment, Hazards.

UNIT V
Memory & Programmable Logic Devices: Digital Logic Families: DTL, DCTL, TTL, ECL & CMOS etc., Fan Out, Fan in, Noise Margin; RAM, ROM, PLA, PAL; Circuits of Logic Families, Interfacing of Digital Logic Families, Circuit Implementation using ROM, PLA and PAL; CPLD and FPGA.

Text Books:

1. M. Morris Mano and M. D. Ciletti, "Digital Design", Pearson Education.
2. David J. Comer, "Digital Logic & State Machine Design", Oxford University Press.
3. RP Jain, "Modern Digital Electronics", Tata McGraw Hill Publication.

ELECTRICAL MACHINES – I

Pre-requisites of course: Basic Electrical Engineering, Engineering Mathematics

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Analyze the various principles & concepts involved in Electromechanical Energy conversion.	K_4
CO2	Demonstrate the constructional details of DC machines as well as transformers, and principle of operation of brushless DC motor, Stepper and DC Servo motors.	K_2
CO3	Evaluate the performance and characteristics of DC Machine as motor and as well as generator.	K_4
CO4	Evaluate the performance of transformers, individually and in parallel operation.	K_4
CO5	Demonstrate and perform various connections of three phase transformers.	K_3

KL- Bloom's Knowledge Level (K₁, K₂, K₃, K₄, K₅, K₆)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I

Pre- Requisites: *Magnetic Materials, BH characteristics*

Principles of Electro-mechanical Energy Conversion: Introduction, Review of magnetic system, Energy in Magnetic system, Force and torque in magnetic field system, Energy balance equation, Energy conversion via electrical field, Energy in a singly excited system, Determination of the Force and Torque from energy and co-energy, Generation of EMF in Machines, Torque in machine with cylindrical air gap.

UNIT II
Pre- Requisites: *Principle & Construction, Classification and circuit model, EMF equation of generator and torque equation of motor*

DC Machines: Armature winding (Concentrated and Distributed), Winding Factor, Armature reaction, Commutation, Interpoles and compensating windings, Performance characteristics of DC generators, Applications.

UNIT III
DC Machines (Contd.): Performance characteristics of DC motors, Starting of DC motors; 3 point and 4 point starters, Speed control of DC motors; Field control, Armature control and Voltage control (Ward Leonard method); Efficiency and Testing of DC machines (Hopkinson's and Swinburne's Test), Applications, ***Introduction to Brushless DC Motor, stepper motor and DC Servo motor and their applications.***

UNIT IV
Pre- Requisites: *Construction & Principle, Ideal and practical transformer, equivalent circuit & phasor diagram, losses in transformers.*

Single Phase Transformer: Efficiency and voltage regulation, all day efficiency, Excitation phenomenon and harmonics in transformers.
Testing of Transformers- O.C. and S.C. tests, Polarity test, Sumpner's test.
Auto Transformer- Single phase and three phase autotransformers, Volt-amp relation, Copper saving in autotransformer Efficiency, Merits & demerits and applications.

UNIT V
Pre- Requisite: *Three-phase connections – Star/Delta.*

Three Phase Transformers: Construction, Three phase transformer, phasor groups and their connections, open delta connection, three phase to 2 phase and their applications, Three winding transformers. Parallel operation of single phase and three phase transformers and load sharing.

Text Books:
1. IJ Nagrath & D.P. Kothari, "Electrical Machines", Tata McGraw Hill
2. Rajendra Prasad , "Electrical Machines", PHI
3. PS Bimbhra, "Electrical Machinery", Khanna Publisher

4. AE Fitggerald, C. Kingsley Jr and Umans, "Electric Machinery", McGraw Hill, International Student Edition.

Reference Books:
1. H. Cotton, "Electrical Technology", CBS Publication.
2. MG Say, "The Performance and Design of AC machines", Pit man& Sons.
3. PS Bimbhra, " Generalized Theory.

NETWORK ANALYSIS & SYNTHESIS

Pre-requisites of course: Basic Electrical Engineering, Basic signal & systems.

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Apply the knowledge of basic circuital law, nodal and mesh methods of circuit analysis and simplify the network using Graph Theory approach.	K_3
CO2	Analyze the AC and DC circuits using Kirchhoff's law and Network simplification theorems.	K_4
CO3	Analyze steady-state responses and transient response of DC and AC circuits using classical and Laplace transform methods.	K_4
CO4	Demonstrate the concept of complex frequency and analyze the structure and function of one and two port network. Also evaluate and analysis two-port network parameters.	K_4
CO5	Synthesize one port network and analyze different filters.	K_4

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

Detailed Syllabus:

UNIT I
Graph Theory:
Pre- Requisites: *Basic circuital law, Mesh & Nodal analysis.*
Importance of Graph Theory in Network Analysis, Graph of a network, Definitions, planar &Non-Planar Graphs, Isomorphism, Tree, Co Tree, Link, basic loop and basic cutset, Incidence matrix, Cut set matrix, Tie set matrix, Duality, Loop and Nodal methods of analysis.

Unit II
AC Network Theorems (Applications to dependent & independent sources):
Pre- Requisites: *Concepts of DC Network Theorems, Electrical Sources&Basic circuital law.*
Superposition theorem, Thevenin's theorem, Norton's theorem, Maximum power transfer theorem, Reciprocity theorem. Millman's theorem, Compensation theorem, Tellegen's Theorem.

Unit III
Transient Circuit Analysis:
Pre- Requisites: *Laplace Transform& Concept of Initial conditions.*

Natural response and forced response, Transient response andsteady state response for arbitrary inputs (DC and AC), Evaluation of time response both through classical and Laplace methods.

Unit IV
Network Functions:
Pre- Requisites: *Concept of basic circuital law, parallel, series circuits.*
Concept of complex frequency, Transform impedances network functions of one port and two port networks, Concept of poles and zeros, Properties of driving point and transfer functions. Two Port Networks- Characterization of LTI two port networks; Z, Y,ABCD, A'B'C'D', g and h parameters, Reciprocity and symmetry, Inter-relationships between the parameters, Inter-connections of two port networks, Ladder and Lattice networks: T & Π representation, terminated two Port networks, Image Impedance.

Unit V
(a)Network Synthesis:
Pre- Requisites: *Laplace Transform, Concept of immittance functions.*
Positive real function; definition and properties, Properties of LC, RC and RL driving point functions, Synthesis of LC, RC and RL driving point immittance functions using Foster and Cauer first and second forms.
(b) Filters
Pre- Requisites: *Concept of Passive & active elements.*
Image parameters and characteristics impedance, Passive and active filter fundamentals, Low pass filters, High pass (constant K type) filters, Introduction to active filters.

Text Books:
1. ME Van Valkenburg, "Network Analysis", Prentice Hall of India.
2. Alexander, Sadiku, "Fundamentals of Electric Circuits", McGraw Hill.
3. D. Roy Choudhary, "Networks and Systems", Wiley Eastern Ltd.
4. CL Wadhwa, "Network Analysis and Synthesis", New Age International Publishers.
5. A. Chakrabarti, "Circuit Theory", Dhanpat Rai & Co.

Reference Books:
1. Hayt, Kimmerly, Durbin, "Engineering Circuit Analysis", McGraw Hill.
2. Donald E. Scott, "An Introduction to Circuit analysis: A System Approach", McGraw Hill.
3. ME Van Valkenburg, "An Introduction to Modern Network Synthesis", Wiley Eastern Ltd.
4. T.S.K.V. Iyer, "Circuit Theory", Tata McGraw Hill.
5. Samarjit Ghosh, " Network Theory: Analysis & Synthesis" Prentice Hall India.

CIRCUIT AND SIMULATION LAB

Pre-requisites of course: Basic Electrical Engineering

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Apply the knowledge of basic circuital law, nodal and mesh analysis for given circuit.	K2
CO2	Analysis of the AC and DC circuits using simulation techniques.	K3
CO3	Analysis of transient response of AC circuits.	K3
CO4	Evaluation and analysis of two-port network parameters.	K2
CO5	Estimation of parameters of different filters.	K2

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

List of Experiments

Ten experiments to be performed

1) Verification of principle of Superposition with AC sources using Multisim/ PSPICE.
2) Verification of Thevenin and Maximum Power Transfer theorems in AC Circuits using Multisim/ PSPICE.
3) Verification of Norton theorems in ACCircuits using Multisim/ PSPICE.
4) Verification of Tellegen's theorem for two networks of the same topology using Multisim/ PSPICE.
5) Determination of Z and h-parameters (DC only) for a network and computation of Y and ABCD Parameters using Multisim/ PSPICE.
6) Determination of driving point and transfer functions of a two port ladder network and verify with theoretical values using Multisim/ PSPICE.
7) Determination of transient response of current in RL and RC circuits with step voltageinput.
8) Determination of transient response of current in RLC circuit with step voltage inputforunder damped, critically damped and over damped cases.
9) Determination of image impedance and characteristic impedance of T and Πnetworks, using O.C. and S.C. tests.

10) Verification of parameter properties in inter-connected two port networks: series, paralleland cascade using Multisim/ PSPICE.

11) Determination of frequency response of a Twin – T-notch filter.

12) To determine attenuation characteristics of a low pass / high pass active filters.

ELECTRICAL MACHINES-I LAB

Pre-requisites of course: Basic Electrical Engineering

Course Outcomes:	Knowledge Level, KL
Upon the completion of the course, the student will be able to:	
CO 1 Analyze and conduct basic tests on DC Machines and single-phase Transformer	K2
CO2 Obtain the performance indices using standard analytical aswell as graphical methods.	K3
CO3 Determine the magnetization, Load and speed-torque characteristics of DC Machines.	K3
CO4 Demonstrate procedures and analysis techniques to perform electromagnetic and electromechanical tests on electrical machines.	K2

KL- Bloom's Knowledge Level (K₁, K₂, K₃, K₄, K₅, K₆)

K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

List of Experiments

Note: Minimum ten experiments are to be performed from the following list, out of which there should be at least two software-based experiments.

1. To obtain magnetization characteristics of a DC shunt generator.
2. To obtain load characteristics of a DC shunt generator andcompound generator (a) Cumulatively compounded (b) Differentially compounded.
3. To obtain efficiency of a DC shunt machine using Swinburne's test.
4. To perform Hopkinson's test and determine losses and efficiency of DC machine.
5. To obtain speed- torque characteristics of a DC shunt motor.
6. To obtain speed control of DC shunt motor using (a) armature resistance control (b) field control
7. To obtain speed control of DC separately excited motor using Ward-Leonard.
8. To obtain equivalent circuit, efficiency and voltage regulation of a single-phase transformer using O.C. and S.C. tests.
9. To obtain efficiency and voltage regulation of a single-phase transformer by Sumpner's test.
10. To obtain 3-phase to2-phase conversion by Scott connection.
11. To demonstrate the parallel operation of three phase transformer and to obtain the load sharing at a load.

Institute may add any two software-based experiments [Develop computer Program in 'C' language

DIGITAL ELECTRONICS LAB

Course Outcomes:		Knowledge Level, KL
Upon the completion of the course, the student will be able to:		
CO 1	Understanding of Digital Binary System and implementation of Gates.	K_2, K_3
CO2	Design the Sequential circuits with the help of combinational circuits and feedback element.	K_3, K_4
CO3	Design data selector circuits with the help of universal Gates.	K_3, K_4
CO4	Design the counters with the help of sequential circuit and basic Gates.	K_3, K_4
CO5	Implement the projects using the digital ICs and electronics components.	K_3, K_5

KL- Bloom's Knowledge Level (K_1, K_2, K_3, K_4, K_5, K_6)
K_1 – Remember K_2 – Understand K_3 – Apply K_4 – Analyze K_5 – Evaluate K_6 – Create

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, Concept of Vcc and ground, verification of the truth tables of logic gates using TTL ICs.

2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.

3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.

4. Implementation and verification of Decoder using logic gates.

5. Implementation and verification of Encoder using logic gates.

6. Implementation of 4:1 multiplexer using logic gates.

7. Implementation of 1:4 demultiplexer using logic gates.

8. Implementation of 4-bit parallel adder using 7483 IC.

9. Design, and verify the 4-bit synchronous counter.

10. Design, and verify the 4-bit asynchronous counter.

11. Implementation of Mini Project using digital integrated circuit's and other components.