DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, LUCKNOW

EVALUATION SCHEME & SYLLABUS

FOR

B. TECH. SECOND YEAR

ELECTRONICS AND COMPUTER ENGINEERING

AS PER

AICTE MODEL CURRICULUM

[Effective from the Session: 2020-21

B.TECH. (ELECTRONICS AND COMPUTER ENGINEERING)

Semester III & IV

Sr. No.	Course Code	Course Title	Periods			Evaluation Scheme				End Semester		Total	Credits
			L	T	P	CT	TA	Total	PS	TE	PE		
	KOE031-38/ KAS302	Engg. Science Course /Maths IV	3	1	0	30	20	50		100		150	4
1.	KAS301/ KVE301	Technical Communication /Universal Human values	2	1	0	30	20	50		100		150	3
			3	0	0								
2.	KEC301	Electronic Devices	3	1	0	30	20	50		100		150	4
3.	KEC302	Digital System Design	3	1	0	30	20	50		100		150	4
4.	KCS303	Discrete Structures & Theory of Logic	3	0	0	30	20	50		100		150	3
6.	KEC351	Electronics Devices Lab	0	0	2				25		25	50	1
7.	KEC352	Digital System Design Lab	0	0	2				25		25	50	1
8.	KCS353	Discrete Structures & Logic Lab	0	0	2				25		25	50	1
9.	KEC354	Mini Project or Internship Assessment	0	0	2			50				50	1
10.	KNC301 /KNC302	Computer System Security /Python Programming	2	0	0	15	10	25		50			0
11.		MOOCs (Essential for Hons. Degree)											
		TOTAL										950	22

*The Mini Project or internship (3-4 weeks) conducted during summer break after II semester and will be assessed during III semester.

Sr. No.	Course Code	Course Title	Periods			Evaluation Scheme				End Semester		Total	Credits
			L	T	P	CT	TA	Total	PS	TE	PE		
1.	KAS402/ KOE041-48	Maths-IV / Engg. Science Course	3	1	0	30	20	50		100		150	4
2.	KVE401/ KAS401	Universal Human Values/ Technical Communication	3	0	0	30	20	50		100		150	3
			2	1	0								
3.	KCS401	Operating Systems	3	0	0	30	20	50		100		150	3
4.	KEC402	Analog Circuits	3	1	0	30	20	50		100		150	4
5.	KEC403	Signal & System	3	1	0	30	20	50		100		150	4
6.	KEC452	Analog Circuits Lab	0	0	2				25		25	50	1
7.	KEC452	Signal System Lab	0	0	2				25		25	50	1
8.	KCS451	Operating Systems Lab	0	0	2				25		25	50	1
9.	KNC402/ KNC401	Python Programming/ Computer System Security	2	0	0	15	10	25		50			0
10.		MOOCs (Essential for Hons. Degree)											
		TOTAL										900	21

KEC301	Electronics Devices	3L:1T:0P	4 Credits

Unit	Topics	Lectures
I	Introduction to semiconductor physics: Review of quantum mechanics, electrons in periodic lattices, E-k diagrams.	8
II	Energy bands in intrinsic and extrinsic silicon, carrier transport, diffusion current, drift current, mobility and resistivity, sheet resistance, design of resistors.	8
III	Generation and recombination of carriers, Poisson and continuity equation P-N junction characteristics, I-V characteristics, and small signal switching models.	8
IV	Avalanche breakdown, Zener diode, Schottky diode, Bipolar Junction Transistor, I-V characteristics, Ebers-Moll model.	8
V	MOS capacitor, C-V characteristics, MOSFET, I-V characteristics, and small signal models of MOS transistor, LED, photodiode and solar cell.	8

Text /Reference Books:

1. G. Streetman, and S. K. Banerjee, “Solid State Electronic Devices,” 7th edition, Pearson, 2014.
2. D. Neamen , D. Biswas, "Semiconductor Physics and Devices," McGraw-Hill Education.
3. S. M. Sze and K. N. Kwok, “Physics of Semiconductor Devices,” 3rd edition, John Wiley &Sons, 2006.
4. C.T. Sah, “Fundamentals of Solid State Electronics,” World Scientific Publishing Co. Inc, 1991.
5. Y. Tsividis and M. Colin, “Operation and Modeling of the MOS Transistor,” Oxford univ. press, 2011.
6. Muhammad H. Rashid, “Electronic Devices and Circuits,” Cengage publication, 2014.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Understand the principles of semiconductor Physics.
2. Understand and utilize the mathematical models of semiconductor junctions.
3. Understand carrier transport in semiconductors and design resistors.
4. Utilize the mathematical models of MOS transistors for circuits and systems.
5. Analyse and find application of special purpose diodes.

KEC302	Digital System Design	3L:1T:0P	4 Credits

Unit	Topics	Lectures
I	Logic simplification and combinational logic design: Binary codes, code conversion, review of Boolean algebra and Demorgans theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, tabulation method.	8
II	MSI devices like comparators, multiplexers, encoder, decoder, driver & multiplexed display, half and full adders, subtractors, serial and parallel adders, BCD adder, barrel shifter and ALU.	8
III	Sequential logic design: Building blocks like S-R, JK and Master-Slave JK FF, edge triggered FF, state diagram, state reduction, design of sequential circuits, ripple and synchronous counters, shift registers, finite state machines, design of synchronous FSM, algorithmic state machines charts. Designing synchronous circuits like pulse train generator, pseudo random binary sequence generator, clock generation.	8
IV	Logic families and semiconductor memories: TTL NAND gate, specifications, noise margin, propagation delay, fan-in, fan-out, tristate TTL, ECL, CMOS families and their interfacing, memory elements, concept of programmable logic devices like FPGA, logic implementation using programmable devices.	8
V	Digital-to-Analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc. analog-to-digital converters (ADC): single slope, dual slope, successive approximation, flash etc. switched capacitor circuits: Basic concept, practical configurations, application in amplifier, integrator, ADC etc.	8

Text/Reference Books:

1. R.P. Jain, "Modern Digital Electronics," Tata McGraw Hill, 4th edition, 2009.
2. A. Anand Kumar, "Fundamental of Digital Circuits," PHI 4th edition, 2018.
3. W.H. Gothmann, "Digital Electronics- An Introduction to Theory and Practice," PHI, 2nd edition, 2006.
4. D.V. Hall, "Digital Circuits and Systems," Tata McGraw Hill, 1989.
5. A. K. Singh, "Foundation of Digital Electronics & Logic Design," New Age Int. Publishers.
6. Subrata Ghosal, "Digital Electronics," Cengage publication, 2nd edition, 2018

Course outcomes: At the end of this course students will demonstrate the ability to:

1. Design and analyze combinational logic circuits.
2. Design and analyze modular combinational circuits with MUX / DEMUX, Decoder & Encoder
3. Design & analyze synchronous sequential logic circuits
4. Analyze various logic families.
5. Design ADC and DAC and implement in amplifier, integrator, etc.

KCS303	Discrete Structures & Theory of	3L:0T:0P	3 Credits

Course Outcome (CO)		
At the end of course , the student will be able to understand		
CO 1	Write an argument using logical notation and determine if the argument is or is not valid.	
CO 2	Understand the basic principles of sets and operations in sets.	
CO 3	Demonstrate an understanding of relations and functions and be able to determine their properties.	
CO 4	Demonstrate different traversal methods for trees and graphs.	
CO 5	Model problems in Computer Science using graphs and trees.	

DETAILED SYLLABUS

Unit	Topic	Proposed Lecture
I	Set Theory: Introduction, Combination of sets, Multisets, Ordered pairs. Proofs of some general identities on sets. Relations: Definition, Operations on relations, Properties of relations, Composite Relations, Equality of relations, Recursive definition of relation, Order of relations. Functions: Definition, Classification of functions, Operations on functions, Recursively defined functions. Growth of Functions. Natural Numbers: Introduction, Mathematical Induction, Variants of Induction, Induction with Nonzero Base cases. Proof Methods, Proof by counter – example, Proof by contradiction.	08
II	Algebraic Structures: Definition, Groups, Subgroups and order, Cyclic Groups, Cosets, Lagrange's theorem, Normal Subgroups, Permutation and Symmetric groups, Group Homomorphisms, Definition and elementary properties of Rings and Fields.	08
III	Lattices: Definition, Properties of lattices – Bounded, Complemented, Modular and Complete lattice. Boolean Algebra: Introduction, Axioms and Theorems of Boolean algebra, Algebraic manipulation of Boolean expressions. Simplification of Boolean Functions, Karnaugh maps, Logic gates, Digital circuits and Boolean algebra.	08
IV	Propositional Logic: Proposition, well formed formula, Truth tables, Tautology, Satisfiability, Contradiction, Algebra of proposition, Theory of Inference. (8) Predicate Logic: First order predicate, well formed formula of predicate, quantifiers, Inference theory of predicate logic.	08
V	Trees: Definition, Binary tree, Binary tree traversal, Binary search tree. Graphs: Definition and terminology, Representation of graphs, Multigraphs, Bipartite graphs, Planar graphs, Isomorphism and Homeomorphism of graphs, Euler and Hamiltonian paths, Graph coloring, Recurrence Relation & Generating function: Recursive definition of functions, Recursive algorithms, Method of solving recurrences. Combinatorics: Introduction, Counting Techniques, Pigeonhole Principle	08

Text books:

1.Koshy, Discrete Structures, Elsevier Pub. 2008 Kenneth H. Rosen, Discrete Mathematics and Its Applications, 6/e, McGraw-Hill, 2006.
2. B. Kolman, R.C. Busby, and S.C. Ross, Discrete Mathematical Structures, 5/e, Prentice Hall, 2004.
3.E.R. Scheinerman, Mathematics: A Discrete Introduction, Brooks/Cole, 2000.
4.R.P. Grimaldi, Discrete and Combinatorial Mathematics, 5/e, Addison Wesley, 2004
5.Liptschutz, Seymour, " Discrete Mathematics", McGraw Hill.
6.Trembley, J.P & R. Manohar, "Discrete Mathematical Structure with Application to Computer Science", McGraw Hill.
4. Deo, 7.Narsingh, "Graph Theory With application to Engineering and Computer.Science.", PHI.
8. Krishnamurthy, V., "Combinatorics Theory & Application", East-West Press Pvt. Ltd., New Delhi

KEC351	Electronic Devices Lab	0L:0T:2P	1 Credits

SUGGESTIVE LIST OF EXPERIMENTS

1. Study of Lab Equipment and Components: CRO, multimeter, and function generator, power supply- active, passive components and bread board.
2. P-N Junction diode: Characteristics of PN junction diode - static and dynamic resistance measurement from graph.
3. Applications of PN Junction diode: Half & Full wave rectifier- Measurement of Vrms, Vdc, and ripple factor.
4. Characteristics of Zener diode: V-I characteristics of Zener diode, graphical measurement of forward and reverse resistance.
5. Characteristics of Photo diode: V-I characteristics of photo diode, graphical measurement of forward and reverse resistance.
6. Characteristics of Solar cell: V-I characteristics of solar cell, graphical measurement of forward and reverse resistance.
7. Application of Zener diode: Zener diode as voltage regulator. Measurement of percentage regulation by varying load resistor.
8. Characteristic of BJT: BJT in CE configuration- graphical measurement of h-parameters from input and output characteristics. Measurement of Av, AI, Ro and Ri of CE amplifier with potential divider biasing.
9. Field Effect Transistors: Single stage common source FET amplifier –plot of gain in dB Vs frequency, measurement of, bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier.
10. Metal Oxide Semiconductor Field Effect Transistors: Single stage MOSFET amplifier –plot of gain in dB Vs frequency, measurement of, bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier.
11. Simulation of amplifier circuits studied in the lab using any available simulation software and measurement of bandwidth and other parameters with the help of simulation software.

Course outcomes: At the end of this course students will demonstrate the ability to:

1. Understand working of basic electronics lab equipment.
2. Understand working of PN junction diode and its applications.
3. Understand characteristics of Zener diode.
4. Design a voltage regulator using Zener diode.
5. Understand working of BJT, FET, MOSFET and apply the concept in designing of amplifiers.

KEC352	Digital System Design Lab	0L:0T:2P	1 Credits

SUGGESTIVE LIST OF EXPERIMENTS

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, Concept of Vcc and ground, verification of the truth tables of logic gates using TTL ICs.
2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.
3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.
4. Implementation and verification of Decoder using logic gates.
5. Implementation and verification of Encoder using logic gates.
6. Implementation of 4:1 multiplexer using logic gates.
7. Implementation of 1:4 demultiplexer using logic gates.
8. Implementation of 4-bit parallel adder using 7483 IC.
9. Design, and verify the 4-bit synchronous counter.
10. Design, and verify the 4-bit asynchronous counter.
11. Implementation of Mini Project using digital integrated circuits and other components.

Course outcomes: At the end of this course students will demonstrate the ability to:

1. Design and analyze combinational logic circuits.
2. Design & analyze modular combinational circuits with MUX/DEMUX, decoder, encoder.
3. Design & analyze synchronous sequential logic circuits.
4. Design & build mini project using digital ICs.

Discrete Structure & Logic Lab (KCS353)

Programming Language/Tool Used: C and Mapple

1. Write a program in C to create two sets and perform the Union operation on sets.
2. Write a program in C to create two sets and perform the Intersectison operation on sets.
3. Write a program in C to create two sets and perform the Difference operation on sets.
4. Write a program in C to create two sets and perform the Symmetric Difference operation.
5. Write a program in C to perform the Power Set operation on a set.
6. Write a program in C to Display the Boolean Truth Table for AND, OR , NOT .
7. Write a C Program to find Cartesian Product of two sets
8. Write a program in C for minimum cost spanning tree.
9. Write a program in C for finding shortest path in a Graph

Note: Understanding of mathematical computation software Mapple to experiment the followings (Exp. 10 to 25):

10. Working of Computation software
11. Discover a closed formula for a given recursive sequence vice-versa
12. Recursion and Induction
13. Practice of various set operations
14. Counting
15. Combinatorial equivalence
16. Permutations and combinations
17. Difference between structures, permutations and sets
18. Implementation of a recursive counting technique
19. The Birthday problem
20. Poker Hands problem
21. Baseball best-of-5 series: Experimental probabilities
22. Baseball: Binomial Probability
23. Expected Value Problems
24. Basketball: One and One
25. Binary Relations: Influence

Write C Programs to illustrate the concept of the following:

1. Sorting Algorithms-Non-Recursive.
2. Sorting Algorithms-Recursive.
3. Searching Algorithm.
4. Implementation of Stack using Array.
5. Implementation of Queue using Array.
6. Implementation of Circular Queue using Array.
7. Implementation of Stack using Linked List.
8. Implementation of Queue using Linked List.
9. Implementation of Circular Queue using Linked List.
10. Implementation of Tree Structures, Binary Tree, Tree Traversal, Binary Search Tree, Insertion and Deletion in BST.
11. Graph Implementation, BFS, DFS, Minimum cost spanning tree, shortest path algorithm.

Semester-IV

KEC403	Signal System	3L:1T:0P	4 Credits
Unit	Topics		Lectures
I	Signals and systems as seen in everyday life, and in various branches of engineering and science, energy and power signals, continuous and discrete time signals, continuous and discrete amplitude signals, system properties: linearity, additivity and homogeneity, shift-invariance, causality, stability, realizability.		8
II	Linear shift-invariant (LSI) systems, impulse response and step response, convolution, input-output behaviour with aperiodic convergent inputs, characterization of causality and stability of linear shift invariant systems, system representation through differential equations and difference equations, Periodic and semi-periodic inputs to an LSI system, the notion of a frequency response and its relation to the impulse response		8
III	Fourier series representation, Fourier transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality , Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier transform (DFT), Parseval's Theorem, the idea of signal space and orthogonal bases, the Laplace transform, notion of Eigen functions of LSI systems, a basis of Eigen functions, region of convergence, poles and zeros of system, Laplace domain analysis, solution to differential equations and system behaviour.		8
IV	The z-Transform for discrete time signals and systems-Eigen functions, region of convergence, z-domain analysis.		8
V	The sampling theorem and its implications- spectra of sampled signals, reconstruction: ideal interpolator, zero-order hold, first-order hold, and so on, aliasing and its effects, relation between continuous and discrete time systems.		8

Text/Reference books:

1. A.V. Oppenheim, A.S. Willsky and I.T. Young, "Signals and Systems," Pearson, 2015.
2. R.F. Ziemer, W.H. Tranter and D.R. Fannin, "Signals and Systems - Continuous and Discrete," 4th edition, Prentice Hall, 1998.
3. B.P. Lathi, "Signal Processing and Linear Systems," Oxford University Press, 1998.
4. Douglas K. Lindner, "Introduction to Signals and Systems," McGraw Hill International Edition: 1999.
5. Simon Haykin, Barry van Veen, "Signals and Systems," John Wiley and Sons (Asia) Private Limited, 1998.
6. V. Krishnaveni, A. Rajeswari, “"Signals and Systems," Wiley India Private Limited, 2012.
7. Robert A. Gabel, Richard A. Roberts, "Signals and Linear Systems," John Wiley and Sons, 1995.
8. M. J. Roberts, "Signals and Systems - Analysis using Transform methods and MATLAB," TMH, 2003.
9. J. Nagrath, S. N. Sharan, R. Ranjan, S. Kumar, "Signals and Systems," TMH New Delhi, 2001.
10. A. Anand Kumar, “Signals and Systems,” PHI 3rd edition, 2018.
11. D. Ganesh Rao, K.N. Hari Bhat, K. Anitha Sheela, “Signal, Systems, and Stochastic Processes,” Cengage publication, 2018.

KEC402	Analog Circuits	3L:1T:0P	4 Credits

Unit	Topics	Lectures
I	Diode circuits, amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier. biasing schemes for BJT and FET amplifiers, bias stability, various configurations (such as CE/CS, CB/CG, CC/CD) and their features, small signal analysis, low frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.	8
II	High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier, various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues, feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.	8
III	Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators.	8
IV	Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load, differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR, Op-Amp design: Design of differential amplifier for a given specification, design of gain stages and output stages, compensation.	8
V	Op-Amp applications: Review of inverting and non-inverting amplifiers, integrator and differentiator, summing amplifier, precision rectifier, Schmitt trigger and its applications, active filters: Low pass, high pass, band pass and band stop, design guidelines.	8

Text/Reference Books:

1. J.V. Wait, L.P. Huelsman and GA Korn, “Introduction to Operational Amplifier theory and applications,” Mc Graw Hill, 1992.
2. J. Millman and A. Grabel, “Microelectronics,” 2nd edition, McGraw Hill, 1988.
3. P. Horowitz and W. Hill, “The Art of Electronics,” 2nd edition, Cambridge University Press, 1989.
4. A.S. Sedra and K.C. Smith, “Microelectronic Circuits,” Saunder's College11 Publishing, 4th edition.
5. Paul R. Gray and Robert G. Meyer, “Analysis and Design of Analog Integrated Circuits,” John Wiley, 3rd edition.
6. Muhammad H. Rashid, “Electronic Devices and Circuits,” Cengage publication, 2014.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Understand the characteristics of diodes and transistors.
2. Design and analyze various rectifier and amplifier circuits.
3. Design sinusoidal and non-sinusoidal oscillators.
4. Understand the functioning of OP-AMP and design OP-AMP based circuits.
5. Design LPF, HPF, BPF, BSF.

KCS401	Operating systems	3L:0T:0P	3 Credits

Course Outcome (CO)		
CO 1	Understand the structure and functions of OS	
CO 2	Learn about Processes, Threads and Scheduling algorithms.	
CO 3	Understand the principles of concurrency and Deadlocks	
CO 4	Learn various memory management scheme	
CO 5	Study I/O management and File systems.	

DETAILED SYLLABUS

Unit	Topic	Proposed Lecture
I	Introduction : Operating system and functions, Classification of Operating systems- Batch, Interactive, Time sharing, Real Time System, Multiprocessor Systems, Multiuser Systems, Multiprocess Systems, Multithreaded Systems, Operating System Structure- Layered structure, System Components, Operating System services, Reentrant Kernels, Monolithic and Microkernel Systems.	08
II	Concurrent Processes: Process Concept, Principle of Concurrency, Producer / Consumer Problem, Mutual Exclusion, Critical Section Problem, Dekker's solution, Peterson's solution, Semaphores, Test and Set operation; Classical Problem in Concurrency- Dining Philosopher Problem, Sleeping Barber Problem; Inter Process Communication models and Schemes, Process generation.	08
III	CPU Scheduling: Scheduling Concepts, Performance Criteria, Process States, Process Transition Diagram, Schedulers, Process Control Block (PCB), Process address space, Process identification information, Threads and their management, Scheduling Algorithms, Multiprocessor Scheduling. Deadlock: System model, Deadlock characterization, Prevention, Avoidance and detection,	08
IV	Memory Management: Basic bare machine, Resident monitor, Multiprogramming with fixed partitions, Multiprogramming with variable partitions, Protection schemes, Paging, Segmentation, Paged segmentation, Virtual memory concepts, Demand paging, Performance of demand paging, Page replacement algorithms, Thrashing, Cache memory organization, Locality of reference.	08
V	I/O Management and Disk Scheduling: I/O devices, and I/O subsystems, I/O buffering, Disk storage and disk scheduling, RAID. File System: File concept, File organization and access mechanism, File directories, and File sharing, File system implementation issues, File system protection and security.	08

Text books:

1. Silberschatz, Galvin and Gagne, "Operating Systems Concepts", Wiley
2. Sibsankar Halder and Alex A Aravind, "Operating Systems", Pearson Education
3. Harvey M Dietel, " An Introduction to Operating System", Pearson Education
4. D M Dhamdhere, "Operating Systems : A Concept based Approach", 2nd Edition,
5. TMH 5. William Stallings, "Operating Systems: Internals and Design Principles ", 6th Edition, Pearson Education

KEC452	Analog Circuit Lab	0L:0T:2P	1 Credits

SUGGESTIVE LIST OF EXPERIMENTS

1. Characteristic of BJT: Study of BJT in various configurations (such as CE/CS, CB/CG, CC/CD).
2. BJT in CE configuration: Graphical measurement of h-parameters from input and output characteristics, measurement of Av, AI, Ro and Ri of CE amplifier with potential divider biasing.
3. Study of Multi-stage amplifiers: Frequency response of single stage and multistage amplifiers.
4. Feedback topologies: Study of voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc.
5. Measurement of Op-Amp parameters: Common mode gain, differential mode gain, CMRR, slew rate.
6. Applications of Op-Amp: Op-Amp as summing amplifier, difference amplifier, integrator and differentiator.
7. Field effect transistors: Single stage common source FET amplifier –plot of gain in dB vs frequency, measurement of bandwidth, input impedance, maximum signal handling capacity (MSHC) of an amplifier.
8. Oscillators: Study of sinusoidal oscillators- RC oscillators (phase shift, Wien bridge etc.).
9. Study of LC oscillators (Hartley, Colpitt, Clapp etc.),
10. Study of non-sinusoidal oscillators.
11. Simulation of amplifier circuits studied in the lab using any available simulation software and measurement of bandwidth and other parameters with the help of simulation software.
12. ADC/DAC: Design and study of Analog to Digital Converter.
13. Design and study of Digital to Analog Converter.

Course Outcome: At the end of this course students will demonstrate the ability to:

1. Understand the characteristics of transistors.
2. Design and analyze various configurations of amplifier circuits.
3. Design sinusoidal and non-sinusoidal oscillators.
4. Understand the functioning of OP-AMP and design OP-AMP based circuits.
5. Design ADC and DAC.

KEC453	Signal System Lab	0L:0T:2P	1

SUGGESTIVE LIST OF EXPERIMENTS

1. Introduction to MATLAB
    a. To define and use variables and functions in MATLAB.
    b. To define and use Vectors and Matrices in MATLAB.
    c. To study various MATLAB arithmetic operators and mathematical functions.
    d. To create and use m-files.
2. Basic plotting of signals
    a. To study various MATLAB commands for creating two and three dimensional plots.
    b. Write a MATLAB program to plot the following continuous time and discrete time signals.
        i. Step Function
        ii. Impulse Function
        iii. Exponential Function
        iv. Ramp Function
        v. Sine Function
3. Time and Amplitude transformations
   Write a MATLAB program to perform amplitude-scaling, time-scaling and time-shifting on a given signal.
4. Convolution of given signals
   Write a MATLAB program to obtain linear convolution of the given sequences.
5. Autocorrelation and Cross-correlation
    a. Write a MATLAB program to compute autocorrelation of a sequence x(n) and verify the property.
    b. Write a MATLAB program to compute cross-correlation of sequences x(n) and y(n) and verify the property.
6. Fourier Series and Gibbs Phenomenon
    a. To calculate Fourier series coefficients associated with Square Wave.
    b. To Sum the first 10 terms and plot the Fourier series as a function of time.
    c. To Sum the first 50 terms and plot the Fourier series as a function of time.
7. Calculating transforms using MATLAB
    a. Calculate and plot Fourier transform of a given signal.
    b. Calculate and plot Z-transform of a given signal.
8. Impulse response and Step response of a given system
    a. Write a MATLAB program to find the impulse response and step response of a system form its difference equation.
    b. Compute and plot the response of a given system to a given input.
9. Pole-zero diagram and bode diagram
    a. Write a MATLAB program to find pole-zero diagram, bode diagram of a given system from the given system function.
    b. Write a MATLAB program to find, bode diagram of a given system from the given system function.

10. Frequency response of a system

    Write a MATLAB program to plot magnitude and phase response of a given system.

11. Checking linearity/non-linearity of a system using SIMULINK
    a. Build a system that amplifies a sine wave by a factor of two.
    b. Test the linearity of this system using SIMULINK.

Course outcomes: At the end of this course students will demonstrate the ability to:

1. Understand the basics operation of MATLAB.
2. Analysis the time domain and frequency domain signals.
3. Implement the concept of Fourier series and Fourier transforms.
4. Find the stability of system using pole-zero diagrams and bode diagram.
5. Design frequency response of the system.

KCS451	Operating Systems Lab	0L:0T:2P	1 Credits

1. Study of hardware and software requirements of different operating systems (UNIX,LINUX,WINDOWS XP, WINDOWS7/8
2. Execute various UNIX system calls for
   i. Process management
   ii. File management
   iii. Input/output Systems calls
3. Implement CPU Scheduling Policies:
   i. SJF
   ii. Priority iii. FCFS
   iv. Multi-level Queue
4. Implement file storage allocation technique:
   i. Contiguous(using array)
   ii. Linked –list(using linked-list)
   iii. Indirect allocation (indexing)
5. Implementation of contiguous allocation techniques:
   i. Worst-Fit
   ii. Best- Fit iii. First- Fit
6. Calculation of external and internal fragmentation
   i. Free space list of blocks from system
   ii. List process file from the system
7. Implementation of compaction for the continually changing memory layout and calculate total movement of data
8. Implementation of resource allocation graph RAG)
9. Implementation of Banker‟s algorithm
10. Conversion of resource allocation graph (RAG) to wait for graph (WFG) for each type of method used for storing graph.
11. Implement the solution for Bounded Buffer (producer-consumer)problem using inter process communication techniques-Semaphores
12. Implement the solutions for Readers-Writers problem using inter process communication technique -Semaphore