

**DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY  
LUCKNOW**



**Evaluation Scheme & Syllabus**

**For**

**B.Tech. 4th Year**

**Electronics and Computer Engineering**

**On**

**AICTE Model Curriculum**

**(Effective from the Session: 2022-23)**

**DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY  
LUCKNOW**

## B.Tech. 4<sup>th</sup> Year VII Semester Electronics and Computer Engineering

S. No.	Course Code	Course Title	Periods			Evaluation Scheme				End Semester		Total	Credits
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KHU 701/ KHU 702	HSMC-1#/ HSMC-2#	3	0	0	30	20	50		100		150	3
2	KECZ 071- 074	Department Elective –IV (From EC Domian)	3	0	0	30	20	50		100		150	3
3	KECZ 075- 079	Department Elective –V (From CS Doamin)	3	0	0	30	20	50		100		150	3
4		Open Elective-II	3	0	0	30	20	50		100		150	3
5	KECZ 751 A/ KECZ 751 B/ KECZ 751 C	Lab as per department electives*	0	0	2				25		25	50	1
6	KECZ 752	Mini Project or Internship Assessment**	0	0	2				50			50	1
7	KECZ 753	Project 1	0	0	8				150			150	4
8		MOOCs (Essential for Hons. Degree)											
		<b>Total</b>										<b>850</b>	<b>18</b>

\*\*The Mini Project or internship (4 weeks) conducted during summer break after VI semester and will be assessed during VII semester.

### Department Elective-IV

KECZ-071- Information Theory & Coding

KECZ-072- VLSI Design

KECZ-073- Wireless & Mobile Communication

KECZ-074- Microwave & Radar Engineering

### Department Elective-V

KECZ-075- Artificial Intelligence

KECZ-076- Internet of Things

KECZ-077- High Performance Computing

KECZ-078- Mobile Computing

KECZ-079- Distributed System

### \*Lab as per Department Elective

KECZ-751A VLSI Design Lab

KECZ-751B Microwave & Radar Engineering Lab

KECZ-751C Department may conduct one lab based on the CS domain elective chosen for the curriculum. The department shall on its own prepare complete list of practical for the lab and arrange for proper setup and conduct accordingly.

\*Students will opt one subject from the list of Department Elective with its corresponding lab.

#Humanities & Social Science including Management Courses

## B.Tech. 4<sup>th</sup> Year VIII Semester

### Electronics and Computer Engineering

S. No.	Course Code	Course Title	Periods			Evaluation Scheme				End Semester		Total	Credits
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KHU-702/ KHU-701	HSMC-2#/ HSMC-1#	3	0	0	30	20	50		100		150	3
2	KOE08X	Open Elective –III	3	0	0	30	20	50		100		150	3
3	KOE08X	Open Elective –IV	3	0	0	30	20	50		100		150	3
4	KECZ 851	Project II	0	0	18				100		300	400	9
5		MOOCs (Essential for Hons. Degree)											
		<b>Total</b>										<b>850</b>	<b>18</b>

**B.Tech. 4<sup>th</sup> Year**  
**Electronics and Computer Engineering**  
**Detailed Syllabus**

<b>KECZ-071</b>	<b>Information Theory &amp; Coding</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>Entropy:</b> Entropy, Joint Entropy and Conditional Entropy, Relative Entropy and Mutual Information, Relationship Between Entropy and Mutual Information, Chain Rules for Entropy, Relative Entropy and Mutual Information, Jensen's Inequality and Its Consequences, Log Sum Inequality and Its Applications, Data-Processing Inequality, Fano's Inequality.	<b>8</b>
<b>II</b>	<b>Asymptotic Equipartition Property:</b> Asymptotic Equipartition Property Theorem. <b>Consequences of the AEP:</b> Data Compression, High-Probability Sets and the Typical Set Data Compression: Examples of Codes, Kraft Inequality, Optimal Codes, Bounds on the Optimal Code Length, Kraft Inequality for Uniquely Decodable Codes, Huffman Codes, Optimality of Huffman Codes, Shannon–Fano–Elias Coding.	<b>8</b>
<b>III</b>	<b>Channel Capacity:</b> Channel Capacity for Various Binary Channels, Symmetric Channels, Properties of Channel Capacity, Preview of Channel Coding Theorem, Jointly Typical Sequences, Channel Coding Theorem, Channel capacity Theorem.	<b>8</b>
<b>IV</b>	<b>Block Codes:</b> Introduction to block codes, Single-parity check codes, Product codes, Repetition codes, Hamming codes, Minimum distance of block codes, Soft-decision decoding, Automatic-repeat-request schemes. <b>Linear Block codes:</b> Definition of linear Block Codes, Generator matrices, Standard array, Parity-check matrices, Error detection and correction.	<b>8</b>
<b>V</b>	<b>Convolution codes:</b> Encoding convolutional codes, Generator matrices for convolutional codes, Generator polynomials for convolutional codes, Graphical representation of convolutional codes, Viterbi Algorithm, Binary Cycle Codes, BCH codes. RS codes, Golay codes.	<b>8</b>

**Text Books:**

1. Bose, Information Theory, Coding and Cryptography, McGraw-Hill Education, 3<sup>rd</sup> Edition, (2 July 2016).
2. Joy A. Thomas, Thomas M. Cover, "Elements of information theory", Wiley-Interscience; 2<sup>nd</sup> edition (July 18, 2006).
3. S. Gravano, "Introduction to Error Control Codes" OUP Oxford (24 May 2001).
4. Robert B. Ash, "Information Theory", Dover Publications (November 1, 1990).
5. Todd k Moon, "Error Correction Coding: Mathematical Methods and Algorithms " Wiley, 2005.

**Reference Books:**

1. Simon Haykin, "Digital communication", John Wiley.
2. Ranjan Bose, "ITC and Cryptography", Tata McGraw-Hill.
3. Roberto Togneri, Christopher J.S deSilva, "Fundamentals of Information Theory and Coding Design", CRC Press.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Explain each block involved in digital communication thoroughly with applications.
2. Apply the knowledge of basic concepts of probability and entropies to analyze the behavior of a communication system.
3. Analyze the use of source coding and evaluating all the techniques of source coding.
4. Examine the significance of channel coding and evaluating all available techniques of channel coding and decoding with challenges.
5. Examine various error control coding techniques.

<b>KECZ-072</b>	<b>VLSI Design</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>Introduction:</b> VLSI Design flow, general design methodologies; critical path and worst case timing analysis, overview of design hierarchy, layers of abstraction, integration density and Moore's law, VLSI design styles, packaging, CMOS Logic, Propagation Delay definitions, sheet resistance.	<b>8</b>
<b>II</b>	<b>Interconnect Parameters:</b> Resistance, Inductance, and Capacitance, skin effect and its influence , lumped RC Model, the distributed RC Model, transient Response, RC delay model, Linear Delay Model, Logical Effort of Paths, Scaling.	<b>8</b>
<b>III</b>	<b>Dynamic CMOS design:</b> steady-state behavior of dynamic gate circuits, noise considerations in dynamic design, charge sharing, cascading dynamic gates, domino logic, np-CMOS logic, problems in single-phase clocking, two-phase non-overlapping clocking scheme, Sequential CMOS Logic Circuits, Layout design.	<b>8</b>
<b>IV</b>	<b>Semiconductor Memories:</b> Dynamic Random Access Memories (DRAM), Static RAM, non-volatile memories, flash memories, Pipeline Architecture. Low – Power CMOS Logic Circuits: Introduction, Overview of Power Consumption, Low – Power Design through voltage scaling,	<b>8</b>
<b>V</b>	<b>Introduction to Testing:</b> Faults in digital circuits. Modeling of faults, Functional Modeling at the Logic Level, Functional Modeling at the Register, Structural Model and Level of Modeling. Design for Testability, Ad Hoc Design for Testability Techniques, Controllability and Observability, Introduction to Built-in-self-test (BIST) Concept.	<b>8</b>

**Text Book:**

1. Sung-Mo Kang & Yosuf Leblebici, "CMOS Digital Integrated Circuits: Analysis & Design", McGraw Hill, 4th Edition.

- Neil H.E.Weste, David Money Harris, "CMOS VLSI Design – A circuits and Systems Perspective" Pearson, 4th Edition.

**Reference Books:**

- D. A. Pucknell and K. Eshraghian, "Basic VLSI Design: Systems and Circuits", PHI, 3rd Ed.,1994.
- R. J. Baker, H. W. Li, and D. E. Boyce , " CMOS circuit design, layout, and simulation", Wiley-IEEE Press,2007.
- M. Abramovici, M.A. Breuer and A.D. Friedman, "Digital Systems and Testable Design" , Jaico Publishing House.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

- Express the concept of VLSI design and CMOS circuits and delay study.
- Analyze mathematical methods and circuit analysis models in analysis of CMOS digital electronics circuits.
- Design and analyze various combinational & sequential circuits based on CMOS technology.
- Examine power logic circuits and different semiconductor memories used in present day technology.
- Interpret faults in digital circuits, Fault Models and various Testing Methodologies.

<b>KECZ-073</b>	<b>Wireless and Mobile Communication</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>Wireless Communication Fundamentals:</b> Evolution of mobile radio communication fundamentals. General Model of Wireless Communication Link, Types of Signals, Cellular Infrastructure, Cellular System Components, Antennas for Cellular Systems, Operation of Cellular Systems, Channel Assignment, Frequency reuse, Channel Assignment strategies, Handoff Strategies Cellular Interferences, Sectorization; Wireless Channel and Radio Communication, Free Space Propagation Model, Channel Noise and Losses, Fading in Land Mobile Systems, Multipath Fading, Fading Effects on Signal and Frequency, Shadowing; Wireless Channel Modeling: AWGN Channel, Rayleigh Channel, Rician Fading Channel, Nakagami Fading Channel, Ocumura and Hata Path Loss Model; Channel Modeling: Stochastic, Flat Fading, Wideband Time-Dispersive Channel Modeling.	<b>8</b>
<b>II</b>	<b>Spread Spectrum and Diversity:</b> Theory of Vocoders, Types of Vocoders; Spread Spectrum Modulation, Pseudo-Noise Codes with Properties and Code Generation Mechanisms, DSSS and FHSS Systems, Time Hopping and Hybrid Spread Systems; Multicarrier Modulation Techniques, Zero Inter Symbol Interference Communication Techniques, Detection Strategies, Diversity Combining Techniques: Selection Combining, Threshold Combining, Equal Gain Combining, Maximum Ratio Combining; Spatial Diversity and	<b>8</b>

	Multiplexing in MIMO Systems, Channel Estimation.	
<b>III</b>	<b>Equalization and Multiple Access:</b> Equalization Techniques: Transversal Filters, Adaptive Equalizers, Zero Forcing Equalizers, Decision Feedback Equalizers, and related algorithms; Multiplexing and Multiple Access: FDMA, TDMA, CDMA, OFDMA, SC-FDMA, IDMA Schemes and Hybrid Method of Multiple Access Schemes, RAKE Receiver; Multiple Access for Radio Packet Systems: Pure ALOHA, Slotted ALOHA, CSMA and their versions; Packet and Pooling Reservation Based Multiple Access Schemes.	<b>8</b>
<b>IV</b>	<b>Cellular Networks:</b> GSM system for mobile Telecommunication, General Packet Radio Service, Edge Technology; CDMA Based Standards: IS 95 to CDMA 2000, Wireless Local Loop, IMT 2000 and UMTS, Long Term Evolution (LTE), Mobile Satellite Communication.	<b>8</b>
<b>V</b>	<b>Other Wireless Networks:</b> Introduction to Mobile Adhoc Networks, Bluetooth, Wi-Fi Standards, WiMax Standards, Li-Fi Communication, Ultra-Wideband Communication, Mobile data networks, Wireless Standards IMT 2000, Introduction to 4G & 5G and concept of NGN.	<b>8</b>

**Text Books:**

1. T.S. Rappaport, "Wireless Communication-Principles and practice", Pearson Publications, Second Edition.
2. Upena Dalal, "Wireless Communication and Networks", Oxford Press Publications, first edition.
3. T L Singal, "Wireless Communications", McGraw Hill Publications, 2010.

**Reference Books:**

1. Andrea Goldsmith, "Wireless Communications", Cambridge University Press, 2005.
2. S. Haykin & M. Moher, "Modern wireless communication", Pearson, 2005.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Express the basic knowledge of mobile radio & cellular communication fundamentals and their application to propagation mechanisms, path loss models and multi-path phenomenon.
2. Analyze the performance of various voice coding and diversity techniques.
3. Apply the knowledge of wireless transmission basics to understand the concepts of equalization and multiple access techniques.
4. Examine the performance of cellular systems being employed such as GSM, CDMA and LTE using various theoretical and mathematical aspects.
5. Express basic knowledge of Mobile Adhoc networks and the existing & upcoming data communication networks in wireless and mobile communication domain.

<b>KECZ-074</b>	<b>Microwave &amp; Radar Engineering</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
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<b>I</b>	<b>Transmission Line:</b> Transmission line equations & solutions, reflection and transmission coefficient, standing wave, standing wave ratio, line impedance and admittance, Introduction to strip lines, Microstrip Transmission line (TL). <b>Wave Guide:</b> Rectangular Wave guide -Field Components and Parameters, TE, TM Modes, Dominant Mode, Circular Waveguides: TE, TM modes. Wave Velocities, Wave guide Cavities.	<b>10</b>
<b>II</b>	<b>Passive microwave devices:</b> Microwave Junctions and Couplers, Scattering Matrix, Passive microwave devices: Microwave Hybrid Circuits, Terminations, Attenuators, Phase Shifters, Microwave Propagation in ferrites, Faraday Rotation, Isolators, Circulators. S parameter analysis of all components.	<b>8</b>
<b>III</b>	<b>Microwave tubes :</b> Microwave Tubes: Limitation of Conventional Active Devices at Microwave frequency, Two Cavity Klystron, Reflex Klystron, Magnetron, Traveling Wave Tube, Backward Wave Oscillators: Their Schematic, Principle of Operation, Performance Characteristic and their applications.	<b>7</b>
<b>IV</b>	<b>Microwave Measurements:</b> Measurement of Insertion Loss, Frequency, Cavity Q, Dielectric Constant, Scattering Parameters, Noise Factors, Return Loss, Impedence; VSWR Metering and Measurement, High Power Measurement; Power Meters, Microwave Amplifiers.	<b>7</b>
<b>V</b>	<b>Introduction to RADAR systems:</b> RADAR Block diagram, RADAR Range equation, Probability of detection of false alarm, Integration of RADAR pulses, RADAR cross section of targets, MTI RADAR, CW RADAR.	<b>8</b>

**Text Books:**

1. Liao, S.Y., " Microwave Devices & Circuits", 3<sup>rd</sup> Edition, Prentice Hall of India Publication, 1995.
2. Sushrut Das, "Microwave Engineering", 1<sup>st</sup> Edition, Oxford University Publication, 2015.
3. M.I. Skolnik, "Introduction to Radar Engineering ", 3<sup>rd</sup> Edition, Tata McGraw Hill Publication, 2001.

**Reference Books:**

1. A Das and S.K. Das, "Microwave Engineering", 1<sup>st</sup> Edition, Tata McGraw Hill Publication, 2000.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Analyze various parameters and characteristics of the transmission line and waveguide and also use of wave guide component as per applications.
2. Describe, analyze and design simple microwave circuits and devices e g couplers, Attenuators, Phase Shifter and Isolators. Student will also understand the microwave propagation in ferrites.
3. Analyze the difference between the conventional tubes and the microwave tubes for



- the transmission of the EM waves.
4. Acquire knowledge about the handling and measurement of microwave equipment.
  5. Differentiate different Radars, find applications and use of its supporting systems.

<b>KECZ-075</b>	<b>ARTIFICIAL INTELLIGENCE</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>INTRODUCTION</b> : Introduction–Definition – Future of Artificial Intelligence – Characteristics of Intelligent Agents– Typical Intelligent Agents – Problem Solving Approach to Typical AI problems.	<b>8</b>
<b>II</b>	<b>PROBLEM SOLVING METHODS:</b> Problem solving Methods – Search Strategies- Uninformed – Informed – Heuristics – Local Search Algorithms and Optimization Problems – Searching with Partial Observations – Constraint Satisfaction Problems – Constraint Propagation – Backtracking Search – Game Playing – Optimal Decisions in Games – Alpha – Beta Pruning – Stochastic Games	<b>8</b>
<b>III</b>	<b>KNOWLEDGE REPRESENTATION:</b> First Order Predicate Logic – Prolog Programming – Unification – Forward Chaining-Backward Chaining – Resolution – Knowledge Representation – Ontological Engineering-Categories and Objects – Events – Mental Events and Mental Objects – Reasoning Systems for Categories – Reasoning with Default Information	<b>8</b>
<b>IV</b>	<b>SOFTWARE AGENTS:</b> Architecture for Intelligent Agents – Agent communication – Negotiation and Bargaining – Argumentation among Agents – Trust and Reputation in Multi-agent systems.	<b>8</b>
<b>V</b>	<b>APPLICATIONS:</b> AI applications – Language Models – Information Retrieval- Information Extraction – Natural Language Processing – Machine Translation – Speech Recognition – Robot – Hardware – Perception – Planning – Moving	<b>8</b>

**Text Books:**

1. S. Russell and P. Norvig, “Artificial Intelligence: A Modern Approach”, Prentice Hall, Third Edition, 2009.
2. I. Bratko, “Prolog: Programming for Artificial Intelligence”, Fourth edition, Addison-Wesley Educational Publishers Inc., 2011.
3. M. Tim Jones, —Artificial Intelligence: A Systems Approach(Computer Science)), Jones and Bartlett Publishers, Inc.First Edition, 2008
4. Nils J. Nilsson, —The Quest for Artificial Intelligence), Cambridge University Press, 2009.

5. William F. Clocksin and Christopher S. Mellish,|| Programming in Prolog: Using the ISO Standard||, Fifth Edition, Springer, 2003.
6. Gerhard Weiss, —Multi Agent Systems||, Second Edition, MIT Press, 2013.
7. David L. Poole and Alan K. Mackworth, —Artificial Intelligence: Foundations of Computational Agents||, Cambridge University Press, 2010.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Describe the basics of the theory and practice of Artificial Intelligence as a discipline and Intelligent agents.
2. Explain search techniques and gaming theory.
3. Implement knowledge representation techniques and problem solving strategies to common AI applications.
4. Compare techniques used for classification and clustering.
5. Evaluate basics of pattern recognition and steps required for it.

<b>KECZ-076</b>	<b>INTERNET OF THINGS</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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Unit	Topics	Lectures
<b>I</b>	<b>Internet of Things (IoT):</b> Vision, Definition, Conceptual Framework, Architectural view, technology behind IoT, Sources of the IoT, M2M Communication, IoT Examples. Design Principles for Connected Devices: IoT/M2M systems layers and design standardization, communication technologies, data enrichment and consolidation, ease of designing and affordability.	<b>8</b>
<b>II</b>	<b>Hardware for IoT:</b> Sensors, Digital sensors, actuators, radio frequency identification (RFID) technology, wireless sensor networks, participatory sensing technology. Embedded Platforms for IoT: Embedded computing basics, Overview of IOT supported Hardware platforms such as Arduino, NetArduino, Raspberry pi, Beagle Bone, Intel Galileo boards and ARM cortex.	<b>8</b>
<b>III</b>	<b>Network &amp; Communication aspects in IoT:</b> Wireless Medium access issues, MAC protocol survey, Survey routing protocols, Sensor deployment & Node discovery, Data aggregation & dissemination	<b>8</b>

<b>IV</b>	<b>Programming the Arduino:</b> Arduino Platform Boards Anatomy, Arduino IDE, coding, using emulator, using libraries, additions in arduino, programming the arduino for IoT.	<b>8</b>
<b>V</b>	<b>Challenges in IoT Design challenges:</b> Development Challenges, Security Challenges, Other challenges IoT Applications: Smart Metering, E-health, City Automation, Automotive Applications, home automation, smart cards, communicating data with H/W units, mobiles, tablets, Designing of smart street lights in smart city.	<b>8</b>

**Text Book:**

1. Olivier Hersent, David Boswarthick, Omar Elloumi "The Internet of Things key applications and protocols", wiley
2. Jeeva Jose, Internet of Things, Khanna Publishing House
3. Michael Miller "The Internet of Things" by Pearson
4. Raj Kamal "INTERNET OF THINGS", McGraw-Hill, 1ST Edition, 2016
5. Arshdeep Bahga, Vijay Madisetti "Internet of Things (A hands on approach)" 1ST edition, VPI publications, 2014
6. Adrian McEwen, Hakin Cassimally "Designing the Internet of Things" Wiley India

**Course Outcome:**

At the end of this course students will demonstrate the ability to:

1. Demonstrate basic concepts, principles and challenges in IoT.
2. Illustrate functioning of hardware devices and sensors used for IoT.
3. Analyze network communication aspects and protocols used in IoT.
4. Execute IoT for developing real life applications using Arduino programming.
5. Develop IoT infrastructure for popular applications.

<b>KECZ-077</b>	<b>HIGH PERFORMANCE COMPUTING</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>Overview of Grid Computing Technology</b> , History of Grid Computing, High Performance Computing, Cluster Computing. Peer-to-Peer Computing, Internet Computing, Grid Computing Model and Protocols, Types of Grids:	<b>8</b>

	Desktop Grids, Cluster Grids, Data Grids, High- Performance Grids, Applications and Architectures of High Performance Grids, High Performance Application Development Environment.	
<b>II</b>	<b>Open Grid Services Architecture:</b> Introduction, Requirements, Capabilities, Security Considerations, GLOBUS Toolkit	<b>8</b>
<b>III</b>	<b>Overview of Cluster Computing:</b> Cluster Computer and its Architecture, Clusters Classifications, Components for Clusters, Cluster Middleware and SSI, Resource Management and Scheduling, Programming, Environments and Tools, Cluster Applications, Cluster Systems,	<b>8</b>
<b>IV</b>	<b>Beowulf Cluster:</b> The Beowulf Model, Application Domains, Beowulf System Architecture, Software Practices, Parallel Programming with MPL, Parallel Virtual Machine (PVM).	<b>8</b>
<b>V</b>	<b>Overview of Cloud Computing:</b> Types of Cloud, Cyber infrastructure, Service Oriented Architecture Cloud Computing Components: Infrastructure, Storage, Platform, Application, Services, Clients, Cloud Computing Architecture.	<b>8</b>

**Text Book:**

1. Laurence T.Yang, Minyi Guo – High Performance Computing Paradigm and Infrastructure John Wiley
2. Ahmar Abbas, “Grid Computing: Practical Guide to Technology & Applications”, Firewall Media, 2004.
3. Joshy Joseph and Craig Fellenstein , “Grid Computing” Pearson Education, 2004.
4. Ian Foster, et al.,“The Open Grid Services Architecture”, Version 1.5 (GFD.80). Open Grid Forum, 2006.
5. RajkumarBuyya. High Performance Cluster Computing: Architectures and Systems. PrenticeHall India, 1999.

**Course Outcome:**

At the end of this course students will demonstrate the ability to:

1. Explain the basic concept of Computer architecture and Modern Processor.
2. Classify the basic concepts of access optimization and parallel computers.
3. Describe different parallel processing platforms involved in achieving high performance computing.
4. Develop efficient and high performance parallel programming.
5. Identify parallel programming using message passing paradigm.

<b>KECZ-078</b>	<b>MOBILE COMPUTING</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	Introduction, issues in mobile computing, overview of wireless telephony: cellular concept, GSM: air-interface, channel structure, location management: HLR-VLR, hierarchical, handoffs, channel allocation in cellular systems, CDMA, GPRS.	<b>8</b>
<b>II</b>	Wireless Networking, Wireless LAN Overview: MAC issues, IEEE 802.11, Blue Tooth, Wireless multiple access protocols, TCP over wireless, Wireless applications, data broadcasting, Mobile IP, WAP: Architecture, protocol stack, application environment, applications.	<b>8</b>
<b>III</b>	Data management issues, data replication for mobile computers, adaptive clustering for mobile wireless networks, File system, Disconnected operations.	<b>8</b>
<b>IV</b>	Mobile Agents computing, security and fault tolerance, transaction processing in mobile computing environment.	<b>8</b>
<b>V</b>	Ad Hoc networks, localization, MAC issues, Routing protocols, global state routing (GSR), Destination sequenced distance vector routing (DSDV), Dynamic source routing (DSR), Ad Hoc on demand distance vector routing (AODV), Temporary ordered routing algorithm (TORA), QoS in Ad Hoc Networks, applications.	<b>8</b>

**Text Book:**

1. J. Schiller, Mobile Communications, Addison Wesley.
2. A. Mehrotra, GSM System Engineering.
3. M. V. D. Heijden, M. Taylor, Understanding WAP, Artech House.
4. Charles Perkins, Mobile IP, Addison Wesley.
5. Charles Perkins, Ad hoc Networks, Addison Wesley.

**Course Outcome:**

At the end of this course students will demonstrate the ability to:

1. Explain and discuss issues in mobile computing and illustrate overview of wireless telephony and channel allocation in cellular systems.
2. Explore the concept of Wireless Networking and Wireless LAN.
3. Analyse and comprehend Data management issues like data replication for mobile computers, adaptive clustering for mobile wireless networks and Disconnected operations.
4. Identify Mobile computing Agents and state the issues pertaining to security and fault tolerance in mobile computing environment.
5. Compare and contrast various routing protocols and will identify and interpret the performance of network systems using Adhoc networks

<b>KECZ-079</b>	<b>DISTRIBUTED SYSTEM</b>	<b>3L:0T:0P</b>	<b>3 Credits</b>
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<b>Unit</b>	<b>Topics</b>	<b>Lectures</b>
<b>I</b>	<b>Characterization of Distributed Systems:</b> Introduction, Examples of distributed Systems, Resource sharing and the Web Challenges. Architectural models, Fundamental Models. Theoretical Foundation for Distributed System: Limitation of Distributed system, absence of global clock, shared memory, Logical clocks ,Lamport's & vectors logical clocks. Concepts in Message Passing Systems: causal order, total order, total causal order, Techniques for Message Ordering, Causal ordering of messages, global state, termination detection.	<b>8</b>
<b>II</b>	<b>Distributed Mutual Exclusion:</b> Classification of distributed mutual exclusion, requirement of mutual exclusion theorem, Token based and non token based algorithms, performance metric for distributed mutual exclusion algorithms. Distributed Deadlock Detection: system model, resource Vs communication deadlocks, deadlock prevention, avoidance, detection & resolution, centralized dead lock detection, distributed dead lock detection, path pushing algorithms, edge chasing algorithms.	<b>8</b>
<b>III</b>	<b>Agreement Protocols:</b> Introduction, System models, classification of Agreement Problem, Byzantine agreement problem, Consensus problem, Interactive consistency Problem, Solution to Byzantine Agreement problem, Application of Agreement problem, Atomic Commit in Distributed	<b>8</b>

	Database system. Distributed Resource Management: Issues in distributed File Systems, Mechanism for building distributed file systems, Design issues in Distributed Shared Memory, Algorithm for Implementation of Distributed Shared Memory.	
<b>IV</b>	<b>Failure Recovery in Distributed Systems:</b> Concepts in Backward and Forward recovery, Recovery in Concurrent systems, Obtaining consistent Checkpoints, Recovery in Distributed Database Systems. Fault Tolerance: Issues in Fault Tolerance, Commit Protocols, Voting protocols, Dynamic voting protocols	<b>8</b>
<b>V</b>	<b>Transactions and Concurrency Control:</b> Transactions, Nested transactions, Locks, Optimistic Concurrency control, Timestamp ordering, Comparison of methods for concurrency control. Distributed Transactions: Flat and nested distributed transactions, Atomic Commit protocols, Concurrency control in distributed transactions, Distributed deadlocks, Transaction recovery. Replication: System model and group communication, Fault - tolerant services, highly available services, Transactions with replicated data.	<b>8</b>

**Text Book:**

1. Singhal & Shivaratri, "Advanced Concept in Operating Systems", McGraw Hill
2. Ramakrishna,Gehrke," Database Management Systems", McGraw Hill
3. Vijay K.Garg Elements of Distributed Computing , Wiley
4. Coulouris, Dollimore, Kindberg, "Distributed System: Concepts and Design", Pearson Education
5. Tenanuanbaum, Steen," Distributed Systems", PHI

**Course Outcome:**

At the end of this course students will demonstrate the ability to:

1. Explain hardware and software issues in modern distributed systems.
2. Classify knowledge in distributed architecture, naming, synchronization, consistency and replication, fault tolerance, security, and distributed file systems.
3. Analyze the current popular distributed systems such as peer-to-peer (P2P) systems.
4. Classify shared Memory Techniques and have sufficient knowledge about file access.
5. Describe knowledge of Synchronization and Deadlock.

<b>KECZ-751A</b>	<b>VLSI DESIGN LAB</b>	<b>0L:0T:2P</b>	<b>1 Credit</b>
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**SUGGESTIVE LIST OF EXPERIMENTS:**

1. Design and analysis of basic of logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR.
2. Design and implementation of Half adder and Full adder using CMOS logic.
3. To simulate the schematic of the common drain amplifier.
4. To simulate the schematic of the differential amplifier.
5. To simulate the schematic of the operational amplifier.
6. Design of 3-8 decoder using MOS technology.
7. Design a 4:1 Multiplexer.
8. Design and implementation of Flip flop circuit.
9. Layout design of PMOS, NMOS transistors.
10. Layout design of CMOS inverter and its analysis.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Design the logic gates.
2. Implementation of combinational and sequential circuits using CMOS logic.
3. Analyze amplifier circuits.
4. Design sequential circuits such as flip flop.
5. Perform the layout designing for physical analysis of the MOS transistor and MOS based circuits.

<b>KECZ-751B</b>	<b>MICROWAVE &amp; RADAR ENGINEERING LAB</b>	<b>0L:0T:2P</b>	<b>1 Credit</b>
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**SUGGESTIVE LIST OF EXPERIMENTS:**

1. To study microwave test bench.
2. To study the characteristics of reflex klystron tube and to determine its electronic tuning range.
3. To determine the frequency and wavelength in a rectangular waveguide working on TE<sub>01</sub> mode.
4. To study measurement of reflection coefficient and standing wave ratio using double minima method.
5. a) To study isolation and coupling coefficient of a magic Tee.  
b) To measure coupling coefficient, Insertion loss & Directivity of a Directional coupler.
6. To study V-I characteristic of Gunn diode.
7. To measure an unknown impedance with Smith chart.
8. a) To measure attenuation and insertion loss of a fixed and variable attenuator.



- b) To measure isolation and insertion loss of a three port Circulators/Isolator.
- 9. Study of Attenuator (Fixed and Variable type).
- 10. To Study working of Doppler radar, and measure the velocity of the object moving in the Radar range.

**Course Outcomes:**

At the end of this course students will demonstrate the ability to:

1. Describe working on microwave testing bench.
2. Practically demonstrate the Characteristics of Reflex klystron using Microwave bench setup.
3. Demonstrate the performance of the Gunn diode using Microwave bench setup.
4. Perform measurement of Frequency, attenuation, VSWR, Impedance of microwave passive device using Klystron Bench Setup.
5. Interpret the basics of Smith chart for solution of transmission line problems and impedance matching.

<b>KECZ-751C</b>	<b>Lab As per Department Elective from CS Domain</b>	<b>0L:0T:2P</b>	<b>1 Credit</b>
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Department may conduct one lab based on the CS domain elective chosen for the curriculum. The department shall prepare complete list of practical for the lab and arrange for proper setup and conduct accordingly on its own.